



PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichia YUDASAKA et al.

Group Art Unit: 2826

Application No.: 09/936,040

Examiner: A. Sefer

Filed: December 11, 2001

Docket No.: 110554

For: A THIN FILM TRANSISTOR AND A METHOD FOR MANUFACTURING
THEREOF

AMENDMENT UNDER 37 C.F.R. §1.111

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In reply to the August 24, 2002 Office Action, please amend the above-identified
application as follows:

IN THE DRAWINGS:

Please amend Figures 13, 14(A)-(E) and 16(A)-(E) as set forth in the attached Request
for Approval of Drawing Corrections.

IN THE CLAIMS:

Please add new claim 19 as follows:

--19. A thin-film transistor, comprising:

a channel region facing a gate electrode through a gate insulating film, the channel
region having a partially distorted portion distorted in a thickness direction of the channel
region;

source and drain regions connection to the channel region in a semiconductor film in
contact with a surface of an insulating substrate; and

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